

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
POWER	2637117
POWERS	81445
CIRCUIT	3201785
CIRCUITS	874387
INTERFACE	741101
INTERFACES	175459
BOARD	900931
BOARDS	217642
(14 AND ((INTERFACE ADJ BOARD) SAME (POWER ADJ CIRCUIT))).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0
(L14 AND POWER ADJ CIRCUIT SAME INTERFACE ADJ BOARD).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L16

[Refine Search](#)[Recall Text](#)[Clear](#)**Search History**DATE: Tuesday, November 25, 2003 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L16</u>	L14 and power adj circuit same interface adj board	0	<u>L16</u>
<u>L15</u>	L14 and power adj circuit same control adj board	1	<u>L15</u>
<u>L14</u>	L10 and power adj circuit same control adj (panel or board or signal\$1)	150	<u>L14</u>
<u>L13</u>	L10 and power adj circuit adj input	7	<u>L13</u>
<u>L12</u>	L11 and power circuit adj input	1	<u>L12</u>
<u>L11</u>	L10 and source adj voltage	112	<u>L11</u>
<u>L10</u>	L5 and power adj circuit	1415	<u>L10</u>
<u>L9</u>	L6 and control adj signal\$1	6	<u>L9</u>
<u>L8</u>	L6 and control adj board	1	<u>L8</u>
<u>L7</u>	L6 and interface adj board	0	<u>L7</u>
<u>L6</u>	L5 and external adj source adj voltage	23	<u>L6</u>
<u>L5</u>	display adj device	339255	<u>L5</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L4</u>	L2 and control adj board	1	<u>L4</u>
<u>L3</u>	L2 and interface adj board	0	<u>L3</u>
<u>L2</u>	L1 and external adj source adj voltage	18	<u>L2</u>
<u>L1</u>	display adj device	77364	<u>L1</u>

END OF SEARCH HISTORY

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
CONTROL	4884150
CONTROLS	1103422
VOLTAGE	1626059
VOLTAGES	320815
INTERFACE	741101
INTERFACES	175459
BOARAD	4
BOARADS	3
((((INTERFACE ADJ BOARAD) SAME (CONTROL ADJ VOLTAGE)) AND 24).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0
(L24 AND CONTROL ADJ VOLTAGE SAME INTERFACE ADJ BOARAD).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L28

[Refine Search](#)[Recall Text](#)[Clear](#)**Search History****DATE:** Tuesday, November 25, 2003 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L28</u>	L24 and control adj voltage same interface adj board	0	<u>L28</u>
<u>L27</u>	L23 and control adj voltage same interface adj board	1	<u>L27</u>
<u>L26</u>	L23 and control voltage same interface adj board	1	<u>L26</u>
<u>L25</u>	L23 and control same voltage and interface adj board	135	<u>L25</u>
<u>L24</u>	L23 and control adj voltage and interface adj board	63	<u>L24</u>
<u>L23</u>	L22 and voltage adj input and interface adj board	156	<u>L23</u>
<u>L22</u>	power and interface adj board	3218	<u>L22</u>
<u>L21</u>	interface adj input adj voltage	6	<u>L21</u>
<u>L20</u>	L19 and variable adj input adj voltage\$1	0	<u>L20</u>
<u>L19</u>	voltage adj input same interface	1096	<u>L19</u>
<u>L18</u>	voltage adj input and interface	9951	<u>L18</u>
<u>L17</u>	variable adj voltage adj input and interface	28	<u>L17</u>
<u>L16</u>	L14 and power adj circuit same interface adj board	0	<u>L16</u>
<u>L15</u>	L14 and power adj circuit same control adj board	1	<u>L15</u>
<u>L14</u>	L10 and power adj circuit same control adj (panel or board or signal\$1)	150	<u>L14</u>
<u>L13</u>	L10 and power adj circuit adj input	7	<u>L13</u>
<u>L12</u>	L11 and power circuit adj input	1	<u>L12</u>
<u>L11</u>	L10 and source adj voltage	112	<u>L11</u>
<u>L10</u>	L5 and power adj circuit	1415	<u>L10</u>
<u>L9</u>	L6 and control adj signal\$1	6	<u>L9</u>
<u>L8</u>	L6 and control adj board	1	<u>L8</u>
<u>L7</u>	L6 and interface adj board	0	<u>L7</u>
<u>L6</u>	L5 and external adj source adj voltage	23	<u>L6</u>
<u>L5</u>	display adj device	339255	<u>L5</u>

DB=USPT; PLUR=YES; OP=ADJ

<u>L4</u>	L2 and control adj board	1	<u>L4</u>
<u>L3</u>	L2 and interface adj board	0	<u>L3</u>
<u>L2</u>	L1 and external adj source adj voltage	18	<u>L2</u>
<u>L1</u>	display adj device	77364	<u>L1</u>

END OF SEARCH HISTORY

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
CONTROL	4884150
CONTROLS	1103422
VOLTAGE	1626059
VOLTAGES	320815
INTERFACE	741101
INTERFACES	175459
BOARAD	4
BOARADS	3
(((INTERFACE ADJ BOARAD) SAME (CONTROL ADJ VOLTAGE)) AND 24).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0
(L24 AND CONTROL ADJ VOLTAGE SAME INTERFACE ADJ BOARAD).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	0

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L28

[Refine Search](#)[Recall Text](#)[Clear](#)**Search History****DATE:** Tuesday, November 25, 2003 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L28</u>	L24 and control adj voltage same interface adj board	0	<u>L28</u>
<u>L27</u>	L23 and control adj voltage same interface adj board	1	<u>L27</u>
<u>L26</u>	L23 and control voltage same interface adj board	1	<u>L26</u>
<u>L25</u>	L23 and control same voltage and interface adj board	135	<u>L25</u>
<u>L24</u>	L23 and control adj voltage and interface adj board	63	<u>L24</u>
<u>L23</u>	L22 and voltage adj input and interface adj board	156	<u>L23</u>
<u>L22</u>	power and interface adj board	3218	<u>L22</u>
<u>L21</u>	interface adj input adj voltage	6	<u>L21</u>
<u>L20</u>	L19 and variable adj input adj voltage\$1	0	<u>L20</u>
<u>L19</u>	voltage adj input same interface	1096	<u>L19</u>
<u>L18</u>	voltage adj input and interface	9951	<u>L18</u>
<u>L17</u>	variable adj voltage adj input and interface	28	<u>L17</u>
<u>L16</u>	L14 and power adj circuit same interface adj board	0	<u>L16</u>
<u>L15</u>	L14 and power adj circuit same control adj board	1	<u>L15</u>
<u>L14</u>	L10 and power adj circuit same control adj (panel or board or signal\$1)	150	<u>L14</u>
<u>L13</u>	L10 and power adj circuit adj input	7	<u>L13</u>
<u>L12</u>	L11 and power circuit adj input	1	<u>L12</u>
<u>L11</u>	L10 and source adj voltage	112	<u>L11</u>
<u>L10</u>	L5 and power adj circuit	1415	<u>L10</u>
<u>L9</u>	L6 and control adj signal\$1	6	<u>L9</u>
<u>L8</u>	L6 and control adj board	1	<u>L8</u>
<u>L7</u>	L6 and interface adj board	0	<u>L7</u>
<u>L6</u>	L5 and external adj source adj voltage	23	<u>L6</u>
<u>L5</u>	display adj device	339255	<u>L5</u>

DB=USPT; PLUR=YES; OP=ADJ

<u>L4</u>	L2 and control adj board	1	<u>L4</u>
<u>L3</u>	L2 and interface adj board	0	<u>L3</u>
<u>L2</u>	L1 and external adj source adj voltage	18	<u>L2</u>
<u>L1</u>	display adj device	77364	<u>L1</u>

END OF SEARCH HISTORY